

<b>Notice of References Cited</b>	Application/Control No. 10/828,895	Applicant(s)/Patent Under Reexamination SRINIVASAN, SANKARANARAYA	
	Examiner Helen Rossoshek	Art Unit 2825	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,286,128 B1	09-2001	Pileggi et al.	716/18
*	B	US-2002/0087940 A1	07-2002	Greidinger et al.	716/2
*	C	US-2003/0009727 A1	01-2003	Takeyama et al.	716/1
*	D	US-6,901,571 B1	05-2005	Petranovic et al.	716/10
*	E	US-2005/0138590 A1	06-2005	Amundson et al.	716/011
*	F	US-6,904,585 B2	06-2005	Brittain et al.	716/13
*	G	US-2005/0289499 A1	12-2005	Ogawa et al.	716/018
*	H	US-7,051,312 B1	05-2006	Rahut et al.	716/13
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hu et al., "A timing-constrained simultaneous global routing algorithm", Sept. 2002, Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on, Volume 21, Issue 9, Page(s):1025 - 1036
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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